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IN THE SPECIFICATION

Please amend paragraph [0086] as follows:

Figure 2 is a high level block diagram of a computer 200 for performing the tasks shown in Figure 1. The computer 200 comprises a processor 210 as well as a memory 220 for storing control programs 221, including clustering algorithms 222, and data structures 223 and the like. The processor 210 cooperates with conventional support circuitry 230 such as power supplies, clock circuits, cache memory and the like as well as circuits that assist in executing the software routines stored in the memory 220. As such, it is contemplated that some of the process steps discussed herein as software processes may be implemented within hardware, for example, as circuitry that cooperates with the processor 210 to perform various steps, or stored on a computer readable medium. The computer also includes input-output circuitry 240 that forms an interface between the various functional elements communicating with the computer 200.